

Design (E) 314

Preliminary Report

PV System Efficiency Monitor

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[01/04/2024)]

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**Abstract (paraphase)**

This report details the design, development, and testing of a multi-functional device tailored to monitor and report PV module performance under varying environmental conditions. User requirements encompassing power measurements, environmental data acquisition, calibration procedures, and real-time communication protocols guided the development process. Key features include precise PV module measurements, ambient environment monitoring, real-time data retrieval, and an intuitive user interface via UART commands and push-buttons. The device's potential impact lies in its ability to empower PV system owners with actionable insights, enhancing system efficiency, and prolonging equipment lifespan. Experimental results validate the device's functionality and reliability in real-world PV system applications, highlighting its practical significance in sustainable energy management.

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**1 Introduction (paraphrase)**

As the adoption of solar photovoltaic (PV) systems grows in South Africa, so does the need for efficient monitoring and maintenance strategies. Dirty or soiled PV modules can significantly reduce power output, impacting the return on investment for system owners. Recognizing this challenge, our project focuses on developing a PV System Efficiency Monitor—a device aimed at helping PV system owners determine the extent of power loss due to dirt and soil accumulation, thereby optimizing cleaning schedules and maximizing energy production.

**2 System description**

**2.1 STM32F303RE microcontroller board**

**2.2 Power Supply**

**2.3 UART Communication**

**2.4 Push – buttons**

**2.5 LEDs**

**2.6 LCD**

**2.7 ADC**

**2.8 Op-Amp**

**2.9 Photodiode**

**2.10 TIP31 BJT (NPN) Transistor**

**2.11 Potentiometer**

**2.12 Analog and Digital Temperature Sensor**

**2.13 PV (Photo voltaic) Panel**

**3 Hardware Design and implementation**

**3.1 Hardware Interaction**

**3.2 Power Supply**

***Requirement*:***The system shall generate its own regulated supplies from a nominal 9 V-12 V battery or power supply. The system is to generate a 5V and 3.3V supply voltage from the power supply.*



Figure 1: 5v Power Supply Diagram

The system takes as input a 9v nominal supply voltage, from which the 7805 regulator in a T0220 package regulates the input voltage down to 5v. The circuit implementation is depicted in Figure 1 above. Components used in the 5v regulator circuit not only include the 7805-voltage regulator but also capacitors C1, C2, and C3 and diode 1N4007.

The capacitors not only serve to filter out noise and high frequency signals from the power supply ensuring a cleaner output, but also smooth out the voltage, reducing voltage ripples, preventing sudden drops in power supply and providing a more stable DC output.

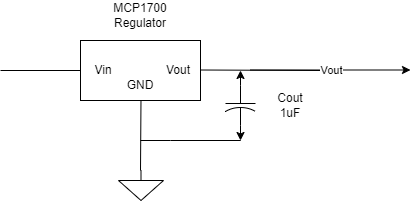


Figure 2: 3.3V Power supply diagram

All the pins on the stm32f411RE micro-controller are not 5v tolerant, for this reason having 3.3V rails is crucial for the rest of the system functionality to be implement. The MCP1700 (3.3v regulator) takes as input the output from the 5v regulator and further reduces the voltage to 3.3V. The minimum and maximum input voltages into the regulator is 2.3V and 6V respectively. The purpose of the capacitor is as described for the 7805-regulator circuit.

**3.3 Push Button**

***Requirement:*** *The system will implement a command interface by using the five push buttons* ***.*** *The push buttons are to be wired as active low, read as inputs by the GPIO pins of the Nucleo Board*

1. *The RIGHT button initiates a calibration procedure*
2. *The LEFT button selects the display mode for the LCD*
3. *The TOP button starts and ends the measurement, calculation, UART transmission, and LCD display of temperatures (Ta, Tsp) and light intensity (L xd) with two presses.*
4. *The BOTTOM button starts and ends the measurement, calculation, UART transmission, and LCD display of PV parameters with two presses.*
5. *The MIDDLE button initiates an RTC clock set menu where TOP and BOTTOM buttons adjust values, and MIDDLE confirms each entry, with the final time reported via UART.*

The buttons are configured as active low configurations, and read as inputs into GPIO pins of the STM32F411RE Nucleo board. On a button press the signal is to be driven low, upon which the system detects that a push button has been pressed. Current limiting resistors have been placed in series with the push buttons, to prevent damage to the Nucleo board.

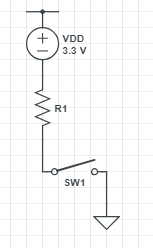


Figure 3: Active Low button configuration

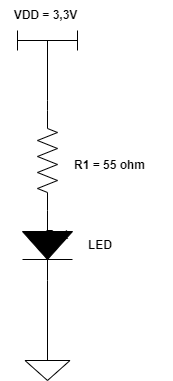
No external circuit need to be built to achieve this active low configuration. The stm32f411 board has a weak internal resistor that pulls the signal high (*STM32F411RE.PDF*). This weak pull-up resistor has a typical resistance of . The top, bottom, left, right and middle buttons are connected to GPIO pins PB9, PB13, PB8, PA7 and PA6 respectively,

**3.4 Debug LEDs**

**LED circuit**

The LED circuit serves to indicate to the user the current state of the system. The LEDs are labeled, D2, D3, D4, and D5, connected to pins PB10, PB4, PB5 and PA10 respectively. The LEDS can be in one of two states:

1. Flashing ON and OFF at a specific rate
2. Remain on



**Figure 4: LED circuit schematic**

The system flashes the LED when a measurement is in progress. Once a measurement stops, the LED corresponding to the devices that are being is stop flashing, and remain on, indicating the end of the measurement.

The LED has a forward current (***If***) of and a forward voltage (***Vf***) of . The voltage supplied from the PINS to the LED circuit is 3.3V (*stm32f411re.pdf*). The pin can sink/source a *±8mA*, and can sink/source a maximum of ±25mA (stm32f411re.pdf) . To prevent any damage to the MCU pin the current is restricted to be 20mA. From this an appropriate resistance value is calculate using:

Thus, an appropriate resistance value for the LED circuit of at least 55Ω is required.

**3.5 LCD Display**

**3.6 ADC**

**3.7 Op-AMP**

**//use in photodiode**

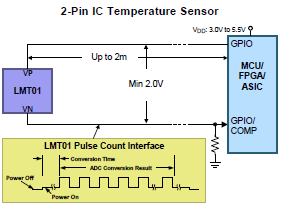
**3.8 PV Panel**

**//refer to cicuit**

**// potentiomter and active load**

**3.7 LMT01 Sensor**

The LMT01 device is a high-accuracy, 2 pin, temperature sensor with an easy-to-use pulse counts current loop interface. The LMT01 has a pulse count interface which is used to determine the temperature. Where the number of output pulses is proportional to the temperature.

**Figure 1: LMT01 top view and pin Figure 2: LMT01 micro-controller connection**

The LMT01 sensor outputs a current pulse that toggles between a high current of 125µA and a low current of 34µA. The LMT01 takes as input 5V source, with the minimum voltage across the sensor to be 5V, with the output of the sensor (VN) connected to pin PA15 on the MCU. For the MCU pin to be able to detect the output voltage from the pulses, the current is converted to an appropriate voltage by calculating an required resistance value. This value determined by the equation:

The micro-controller detects as input a low signal that is less than **0.3VDD** and an input high voltage (VIH) that is a minimum of **0.7VDD** (*stm32f411re.pdf pg 98*). VDD falls in the range of [1.7V, 3.6V] (*stm32f411re.pdf*). For VDD = 3.6V it is determined that the high input voltage should be greater than 2.52V and the low input voltage should be less than 1.08V maximum. Thus the minimum resistance value for the high current of 125µA is determined by:

For the designed circuit, a resistance value of 22kΩ is chosen which meets the above threshold. To verify that the voltage (**VIL**) from the low current (34µA) is within the maximum threshold of 1.08V, we compute:

To verify the voltage (VIH) from the high voltage is above it’s minimum threshold 2.52V, we compute:

The above proves that for the low and high output current from the LMT01, using a resistor value of 22kΩ they have been converted to valid logic level, able to be detected by the MCU.

**4 Software Design and Implementation**

**4.1 Software Interaction**

**4.2 LCD Driver setup and integration**

**//lcd driver setup**

**//lcd modes**

**4.3 Push button debouncing**

**4.4 UART Communication**

**4.5 ADC (Processing of data and use cases)**

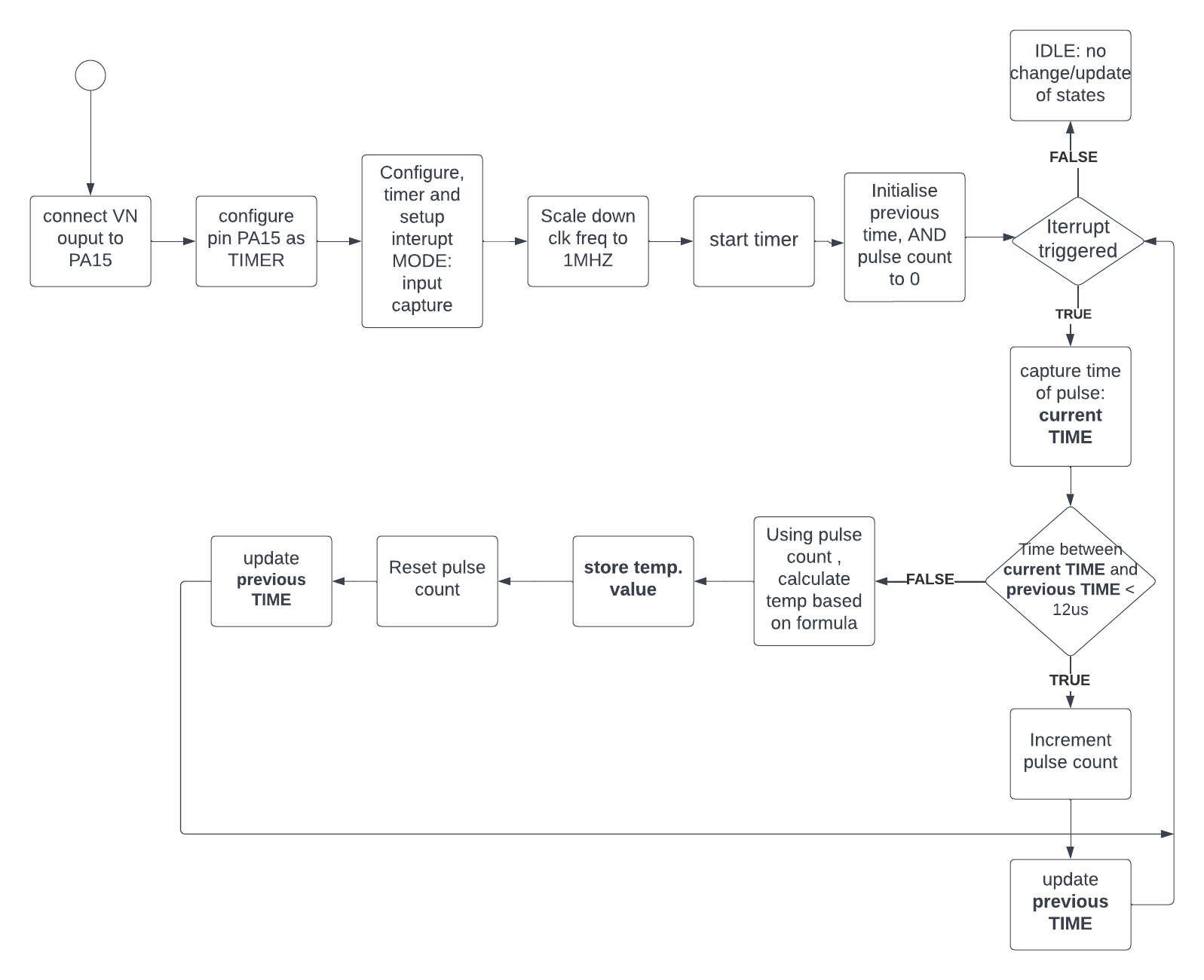
**4.6 LMT01 Software Design and System Interaction**

**LMT01 description**

The LMT01 had a window period of maximum 104ms in which the measured temperature by the device will be output as pulses. At every 104ms window period, the temperature is to be determined, while being cautious of overlapping window period readings.

The output of the average temp of the LMT01is to be within 3 degrees of the measured temperature of the testing station. The temperature from the LMT01 sensor is to change appropriately when the sensor is touched. This requirement is met and is proved in the next section.

**Software design**



**Figure 5: LT01 software design – Measurement and Conversion**

To setup the software for the LMT01 sensor we begin by configuring the required peripheral for the LMT01 sensor via the STM32CUBEIDE ***.ioc*** file. GPIO Pin PA15 is set as timer PIN, with interrupts enabled by checking the NVIC box in the parameter settings. Next this interrupt is configured to be triggered on the rising edge with the mode of the timer to be in Input Capture mode for reasons that follow.

TIMER PINS can be set in one of 4 modes, namely; Input Capture, PWM mode, One Pulse Mode and Output compare Mode, each with their own functions. Of interest is the Input capture. This mode is to be used to capture the time at which interrupts occur, which enables us to keep track of the windowing periods. PIN PA15 is setup as a timer in input capture mode, where PA15 is connected to timer 2 CHANNEL 1.

The hardware has been setup such that the output current from the LTM01 is converted to valid voltage logic levels. Each pulse will trigger the interrupt. The LMT01 sensor outputs pulses at 88khz, from this the calculated period of each pulse is approximately 11.36us ~ 12us. Because the period of each pulse is in the micro-second range, the timer clock is configured to count in **µs** (micro-seconds). For this, Timer 2 (timer that PA15 is connected to) frequency is scaled down from 84Mhz to a 1Mhz signal.

The timer is started and the approach taken is to measure the time between consecutive pulses. Noting that within a window period, a pulse is expected every 12µs once the pulse train begins. For any measured time larger than 12µs, it means that the next pulse window has started, thus the count is to be restarted and the temperature to be recalculated.

Within each window period, that temperature of that window is determined and only at the start of the next window period.

After the temperature has been converted, it’s value is stored (ready to be processed), the pulse count is reset, and the pulses at the next window period begins incrementing with every pulse received and again at the start of the next window period, the temperature is again re-calculated for previous window period.

The temperatures are calculated based off the formula, where PC is the number of pulses:



**Figure 6: Temperature calculation formula**

This process is repeated indefinitely. Note that all of this is happening inside the interrupt handler function. Where the interrupt is triggers at every pulse recored. The stored temperature is ready to be used for further processing at any time.

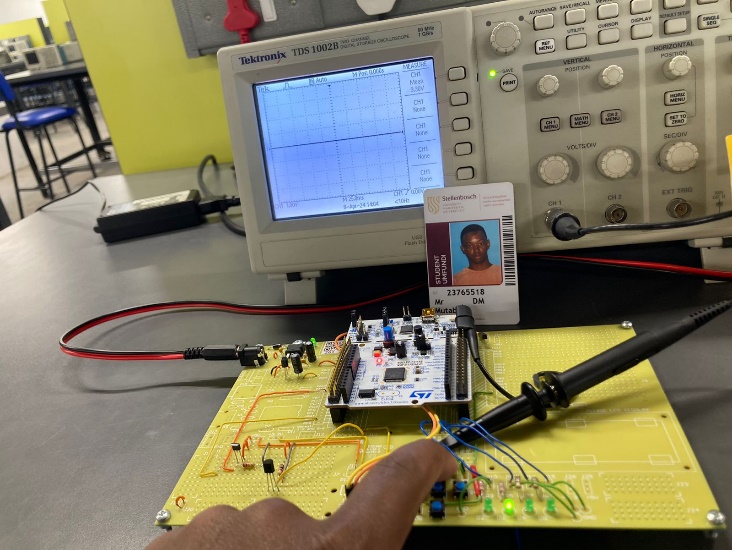
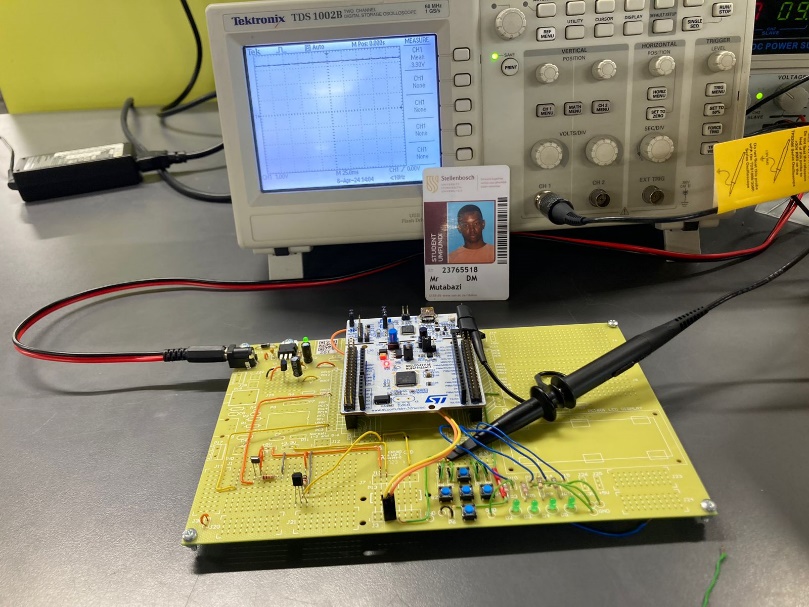
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**Figure 7: Input capture interrupt callback function**

**5 Measurements and Results**

* 1. **Power Supply**
  2. **UART Communication**
  3. **Button Circuit**

**Top push button - active low**

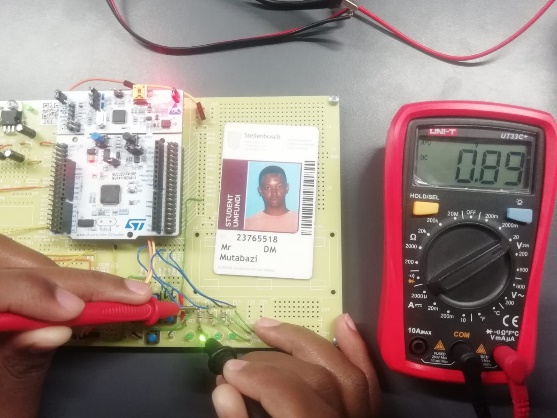


**Figure 11: Button Not pressed Figure 12: Button Pressed**

Measuring using a multimeter/and oscilloscope, Figure 11 shows us that when the top button is not pressed the pin is at a high signal. This confirms the presence of an internal pull-up resistor pulling the signal high. When the button is pressed, Figure 12 shows us the state of the signal, dropping low. From this we can confirm the active low circuit connection.

* 1. **LED circuit**

**LED circuit**

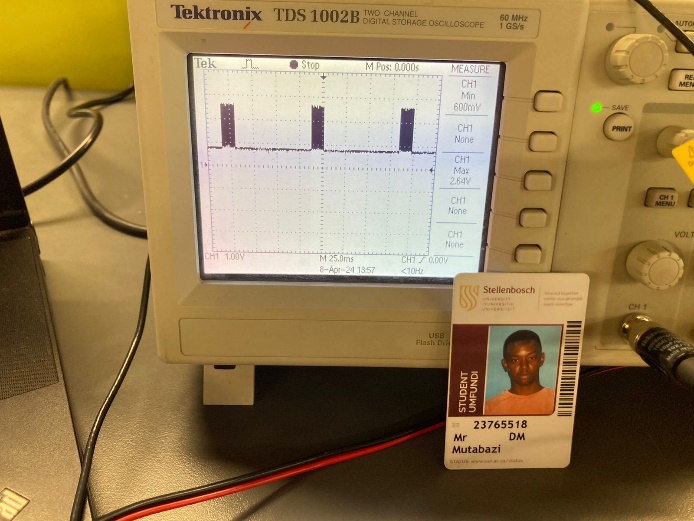
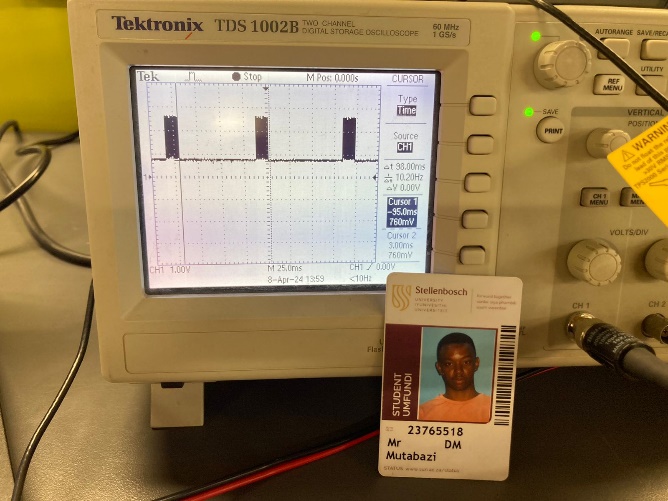


**Figure 12: Voltage across LED**

Figure 12 illustrates the voltage measurement across the resistor to be 0.89V, from which the current is measured using ohms law. The resulting current in the circuit is **15mA** which is less than maximum current supplied by the pins of 25mA

* 1. **ADC**
  2. **LCD**
  3. **PV Panel**
  4. **Photodiode and Op-Amp**
  5. **LMT01**

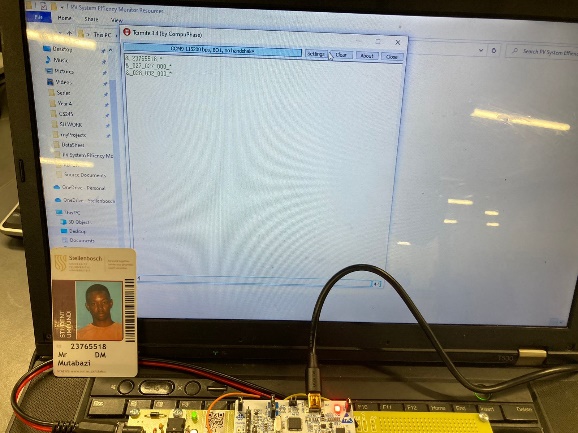
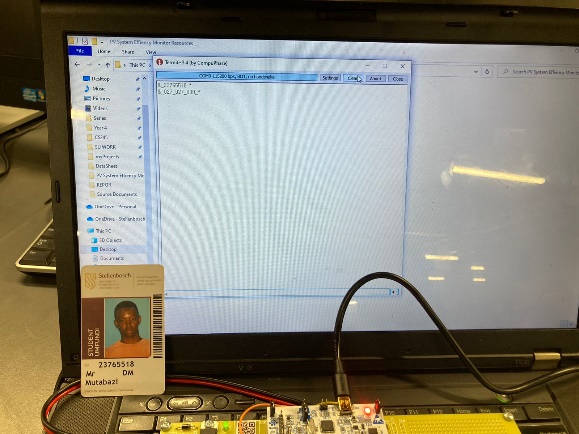
The above image are the pulse outputs for the LMT01 sensor, where the pulse window period is roughly almost 104ms as shown by the oscilloscope. Note that it is not exactly 104ms as it is not clear after the pulse go low, when the next period starts. It is also worth noting that indeed the pulse period is roughly 88khz.



**Figure 8: LMT01 pulse outputs and low and high voltage**

The measured high voltage and low voltage was measured with an oscilloscope, showing that the voltages were at the appropriate voltage levels to be detected by the STM32 pin. Where the high voltage was **2.75v** and low voltage **0.75V**. Figure 9 below shows this:

* 1. **LMT01 Functionality – Reaction to changing temperature**



**Figure 13: LMT01 sensor: Not touched Figure 14: LMT01 sensor: Touched**

From the termite output, we observe the temperature measured by the LMT01 when it is not touched (Figure 13) and when it is touched (Figure 14). From this we observe that the sensor temperature changes appropriately when changed and meets the necessary requirements.

Based on the results produced from these measurements, the system meets the necessary requirements.

* 1. **Complete System**

**6 Conclusions**

**6.1 Non-Compliance and Short comings**

**6.2 Possible Improvements**

**REFERENCES**

[1] STMicroelectronics, "RM0383, Reference manual: STM32F411xC/E advanced Arm®-based 32-bit MCU," [Online]. Available: [https://www.st.com].

[2] STMicroelectronics, "STM32F411xC STM32F411xE Arm® Cortex®-M4 32b MCU+FPU, 125 DMIPS, 512KB Flash, 128KB RAM, USB OTG FS, 11 TIMs, 1 ADC, 13 comm. interfaces," [Online]. Available: [https://www.st.com]